

March 2013

FDA20N50F

N-Channel UniFETTM FRFET[®] MOSFET 500 V, 22 A, 260 m Ω

Features

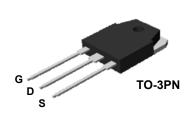
- $R_{DS(on)}$ = 220 m Ω (Typ.) @ V_{GS} = 10 V, I_{D} = 11 A
- Low Gate Charge (Typ. 50 nC)
- Low C_{rss} (Typ. 27 pF)
- · 100% Avalanche Tested
- · Improved dv/dt Capability
- · RoHS Compliant

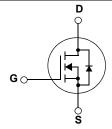
Applications

- PDP TV
- · Uninterruptible Power Supply
- · AC-DC Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor[®]'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. The body diode's reverse recovery performance of UniFET FRFET[®]MOSFET has been enhanced by lifetime control. Its trr is less than 100nsec and the reverse dv/dt immunity is 15V/ns while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore, it can remove additional component and improve system reliability in certain applications in which the performance of MOSFET's body diode is significant. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted*

	•				
Symbol		Parameter		FDA20N50F	Unit
V _{DSS}	Drain to Source Voltage			500	V
V _{GSS}	Gate to Source Voltage			±30	V
	Drain Current	- Continuous (T _C = 25°C)		22	^
ID	Diamounent	- Continuous (T _C = 100°C)		13	Α
I _{DM}	Drain Current	- Pulsed	- Pulsed (Note 1)		
E _{AS}	Single Pulsed Avalanche En	ergy	(Note 2)	1110	mJ
I _{AR}	Avalanche Current		(Note 1)	22	Α
E _{AR}	Repetitive Avalanche Energy (1		(Note 1)	39	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	20	V/ns
D	Davier Discipation	$(T_C = 25^{\circ}C)$		388	W
P_{D}	Power Dissipation	- Derate above 25°C		3.1	W/°C
T _J , T _{STG}	Operating and Storage Tem	perature Range		-55 to +150	°C
T _L	Maximum Lead Temperature 1/8" from Case for 5 Second	• •		300	°C

Thermal Characteristics

Symbol	Parameter Max.		Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.44	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max. 40		-0/00

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDA20N50F	FDA20N50F	TO-3PN	-	-	30

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0 V$, $T_J = 25 ^{\circ} C$	500	-	-	V
ΔBV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.6	-	V/°C
ı	Zero Gate Voltage Drain Current	V _{DS} = 500V, V _{GS} = 0V	-	-	10	
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 400V, T_C = 125^{\circ}C$	-	-	100	μА
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	3.0	-	5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 11A$	-	0.22	0.26	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 40V, I_{D} = 11A$	-	24	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 051/1/ 01/	-	2550	3390	pF
C _{oss}	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ f = 1MHz	-	350	465	pF
C _{rss}	Reverse Transfer Capacitance	1 - 111112	-	27	40	pF
Q _{g(tot)}	Total Gate Charge at 10V		-	50	65	nC
Q_{gs}	Gate to Source Gate Charge	$V_{DS} = 400V, I_{D} = 20A$	-	14	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	V _{GS} = 10V (Note 4)	-	20	-	nC

Switching Characteristics

	_						
t _{d(on)}	Turn-On Delay Time			-	45	100	ns
t _r	Turn-On Rise Time	$V_{DD} = 250V, I_{D} = 20A$		-	120	250	ns
t _{d(off)}	Turn-Off Delay Time	$R_G = 25\Omega$		-	100	210	ns
t _f	Turn-Off Fall Time		(Note 4)	-	60	130	ns

Drain-Source Diode Characteristics

I_S	Maximum Continuous Drain to Source Diode Forward Current			-	22	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current			-	88	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 22A	-	-	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 20A	-	154	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	-	0.5	-	μС

Notes:

- **Notes:**1. Repetitive Rating: Pulse width limited by maximum junction temperature 2: L = 5mH, $I_{AS} = 20A$, $V_{DD} = 50V$, $R_{G} = 25\Omega$, Starting $T_{J} = 25^{\circ}C$ 3: $I_{SD} \le 22A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_{J} = 25^{\circ}C$ 4: Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

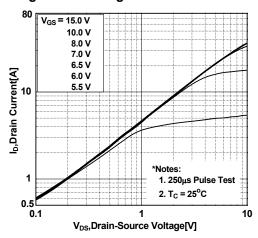


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

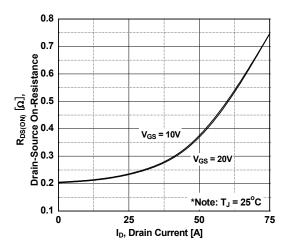


Figure 5. Capacitance Characteristics

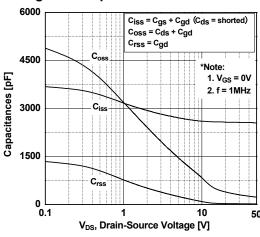


Figure 2. Transfer Characteristics

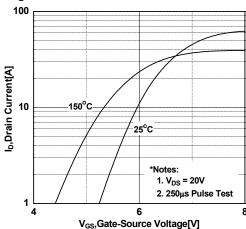


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

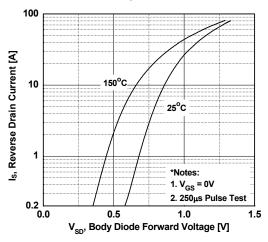
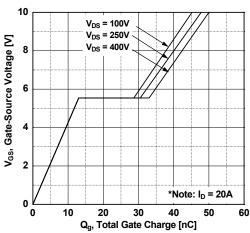


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

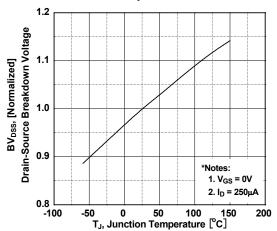


Figure 9. Maximum Drain Current vs. Case Temperature

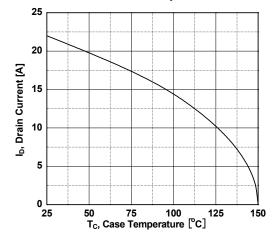


Figure 8. Maximum Safe Operating Area

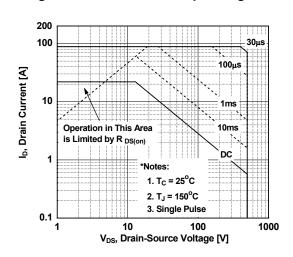
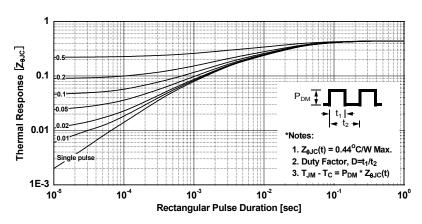
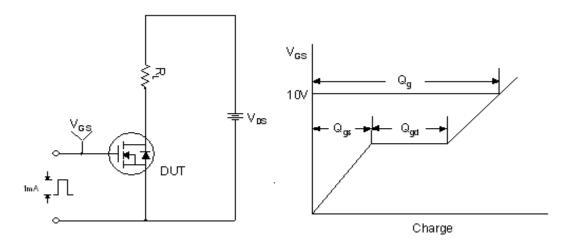


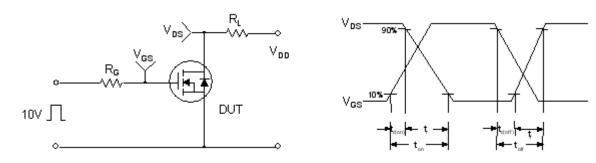
Figure 10. Transient Thermal Response Curve



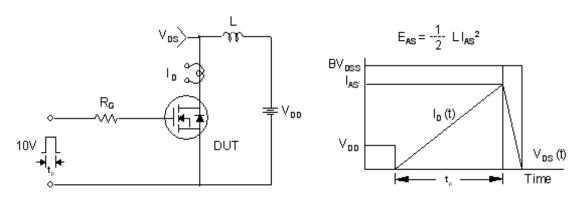
Gate Charge Test Circuit & Waveform



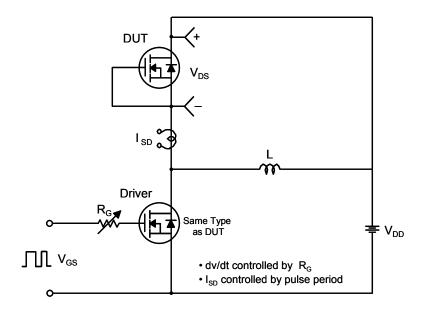
Resistive Switching Test Circuit & Waveforms

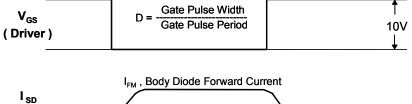


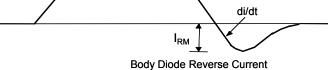
Unclamped Inductive Switching Test Circuit & Waveforms

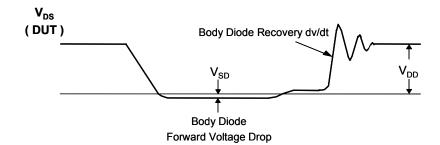


Peak Diode Recovery dv/dt Test Circuit & Waveforms





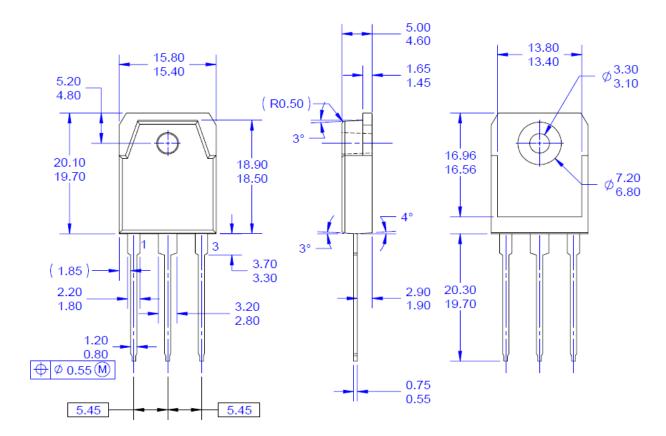


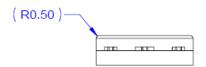


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Mechanical Dimensions

TO-3PN





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